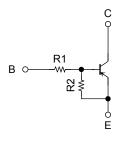
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

RN2101CT, RN2102CT, RN2103CT RN2104CT, RN2105CT, RN2106CT

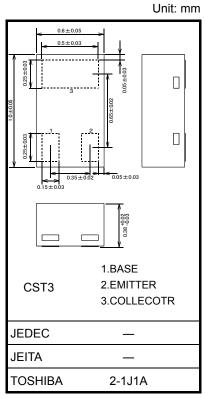
Switching Applications
Inverter Circuit Applications
Interface Circuit Applications
Driver Circuit Applications

- Incorporating a bias resistor into a transistor reduces parts count.
 Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Complementary to RN1101CT to RN1106CT

Equivalent Circuit and Bias Resistor Values



Type No.	R1 (kΩ)	R2 (kΩ)
RN2101CT	4.7	4.7
RN2102CT	10	10
RN2103CT	22	22
RN2104CT	47	47
RN2105CT	2.2	47
RN2106CT	4.7	47



Weight: 0.75 mg (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage	RN2101CT to 2106CT	V_{CBO}	-20	V	
Collector-emitter voltage	1010101010001	V _{CEO}	-20	V	
Emitter-base voltage	RN2101CT to 2104CT	Vene	-10	V	
	RN2105CT, 2106CT	V _{EBO}	-5		
Collector current		IC	-50	mA	
Collector power dissipation	RN2101CT to 2106CT	PC	50	mW	
Junction temperature	KN2101C1 t0 2100C1	Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.operatingtemperature/current/voltage, etc.) are within the absolute maximum ratings.

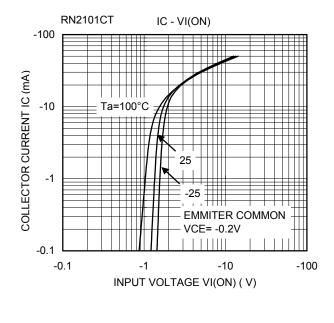
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

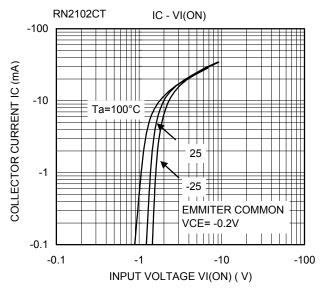
Start of commercial production 2004-10

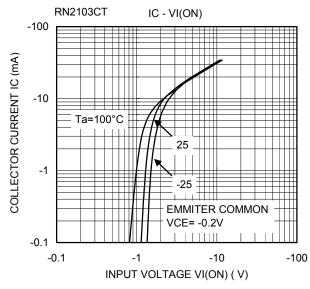


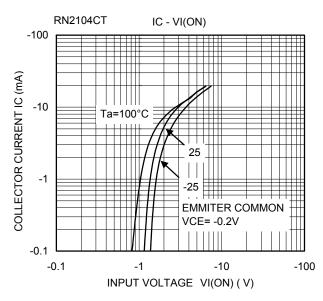
Electrical Characteristics (Ta = 25°C)

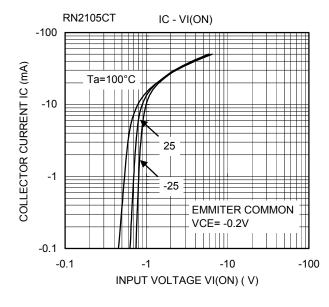
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	RN2101CT to 2106CT	I _{CBO}	$V_{CB} = -20 \text{ V}, I_E = 0$	_	_	-100	nA
	14421010110210001	I _{CEO}	$V_{CE} = -20 \text{ V}, I_B = 0$	_	_	-500	11/5
	RN2101CT	I _{EBO}	V _{EB} = -10 V, I _C = 0	-0.89	_	-1.33	mA
	RN2102CT			-0.41	_	-0.63	
Emitter out off current	RN2103CT			-0.18	_	-0.29	
Emitter cut-off current	RN2104CT			-0.088	_	-0.133	IIIA
	RN2105CT		V _{EB} = -5 V, I _C = 0	-0.085	_	-0.127	
	RN2106CT		vEB = -2 v, IC = 0	-0.08	_	-0.121	
	RN2101CT			30	_	_	
	RN2102CT			60	_	_	
DC aumant rain	RN2103CT	_	$V_{CE} = -5 V$,	100	_	_	
DC current gain	RN2104CT	h _{FE}	I _C = -10 mA	120	_	_	
	RN2105CT			120	_	_	
	RN2106CT			120	_	_	
Collector-emitter saturation voltage	RN2101CT to 2106CT	V _{CE} (sat)	$I_C = -5 \text{ mA},$ $I_B = -0.25 \text{ mA}$	_	_	-0.15	V
	RN2101CT	VI (ON)	$V_{CE} = -0.2 \text{ V},$ $I_{C} = -5 \text{ mA}$	-1.0	_	-2.0	٧
	RN2102CT			-1.0	_	-2.2	
Input voltage (ON)	RN2103CT			-1.1	_	-2.7	
Input voltage (ON)	RN2104CT			-1.2	_	-3.6	
	RN2105CT			-0.6	_	-1.1	
	RN2106CT			-0.6	_	-1.2	
Input voltage (OFF)	RN2101CT to 2104CT	V _{I (OFF)}	V _{CE} = -5 V, I _C = -0.1 mA	-0.8	_	-1.5	V
	RN2105CT, 2106CT			-0.4	_	-0.8	
Collector output capacitance	RN2101CT to 2106CT	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0,$ f = 1 MHz	_	1.2	_	pF
Input resistor	RN2101CT	R1	_	3.76	4.7	5.64	- kΩ
	RN2102CT			8	10	12	
	RN2103CT			17.6	22	26.4	
	RN2104CT			37.6	47	56.4	
	RN2105CT			1.76	2.2	2.64	
	RN2106CT			3.76	4.7	5.64	
Resistor ratio	RN2101CT to 2104CT	R1/R2	_	0.8	1.0	1.2	
	RN2105CT			0.0376	0.0468	0.0562	
	RN2106CT			0.08	0.1	0.12	

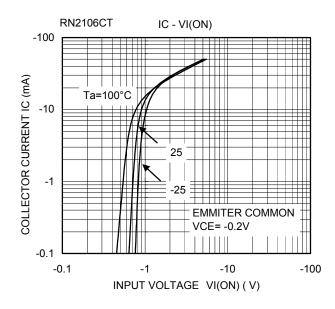


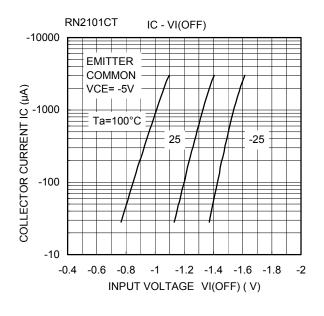


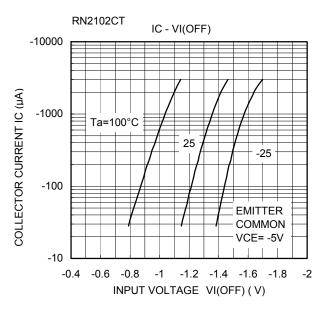


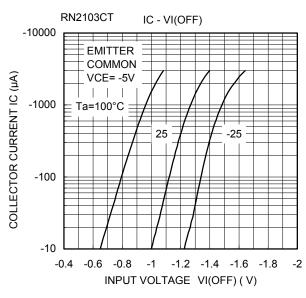


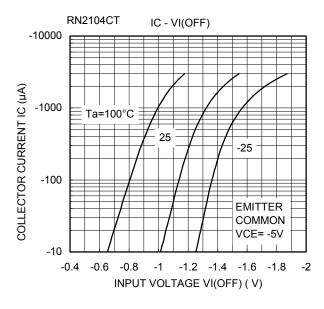


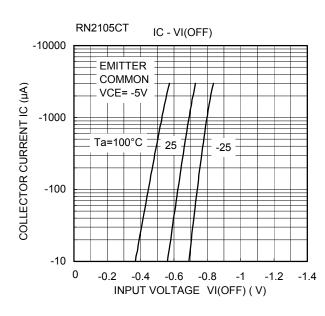


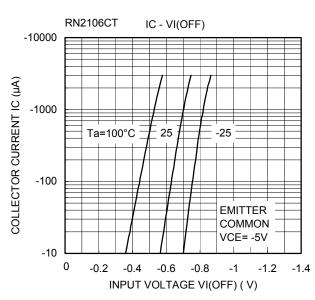


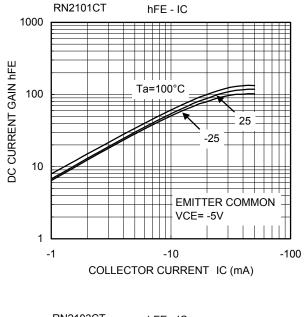


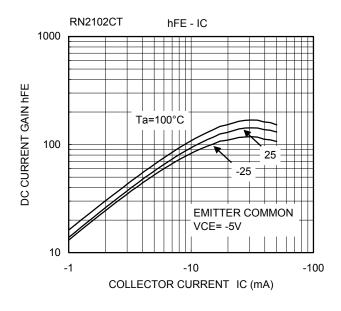


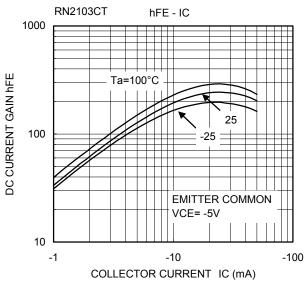


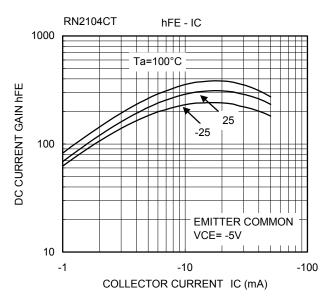


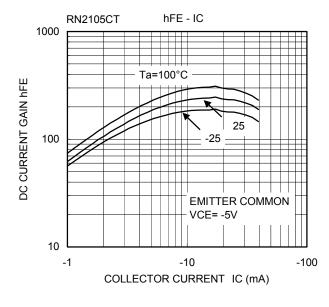


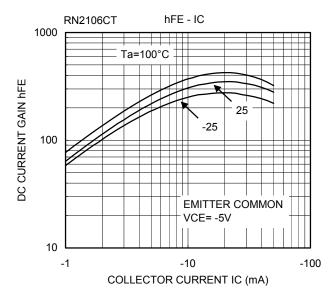


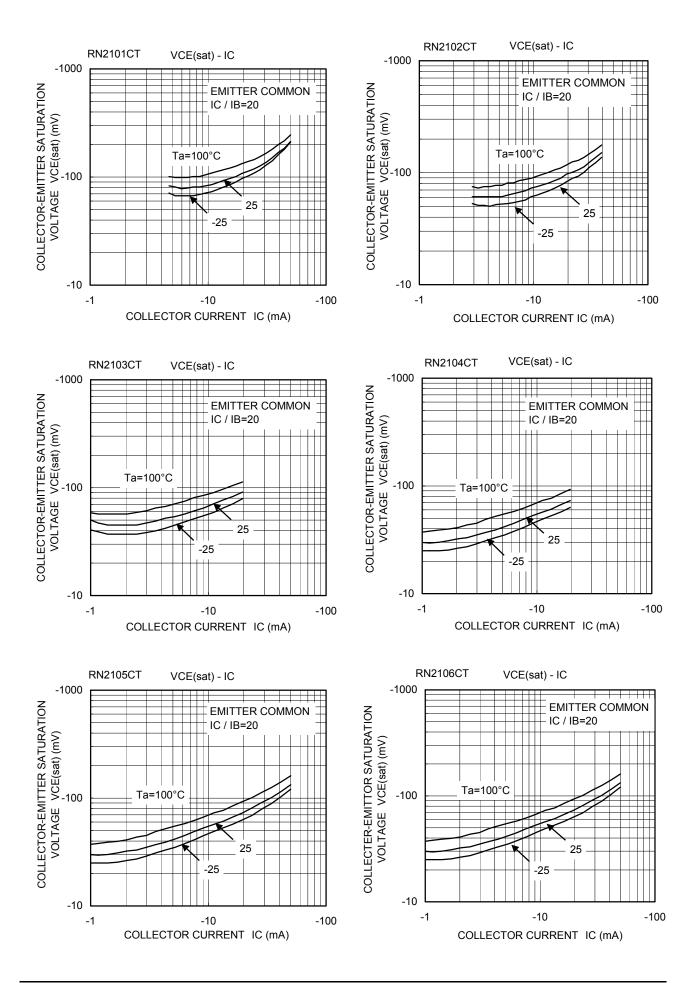












Type Name	Marking
RN2101CT	Type name 1 2 U0 3
RN2102CT	Type name 1 2 U1 3
RN2103CT	Type name 1 U2 3
RN2104CT	Type name 1 2 U3 3
RN2105CT	Type name 1 2
RN2106CT	Type name 1 U5 3

Handling Precaution

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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